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No.:

APPLICANT(s): VANDENHOECK et al.

SERIAL NO.:

ART UNIT:

FILING DATE: Herewith

EXAMINER:

TITLE: POWER EFFICIENT SYMBOL PROCESSING

ATTORNEY

DOCKET NO.: 871-011564-US (PAR) / 31020064 US02

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

(37 C.F.R. §1.97(b)(1))

Sir:

This Information Disclosure Statement is filed together with the above-identified patent application. Thus, a certification under 37 C.F.R. 1.97 (e) or fee under 37 C.F.R. 1.17(p) is not required for the information herein to be considered.

The following information is being disclosed to the Patent and Trademark Office as information that may be material to the examination of the above-identified patent application.

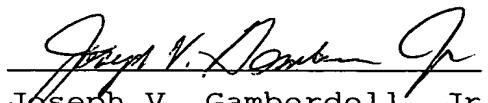
The above-identified patent application claims priority to European Patent Application No. EP 02257509.6 filed on

October 30, 2002. Applicants' attorney encloses a copy of a European Search Report issued on the priority European Patent Application No. EP 02257509.6. The Search Report cited United States Patent Application Publication No. 2001/0004761, PCT International Publication No. WO 01/93431 and "A 16-Bit Digital Signal Processor With Specially Arranged Multiply-Accumulator For Low Power Consumption", Katsuhiko et al., IEICE Transactions on Electronics, Institute Of Electronics Information And Comm. Eng., Vol. E78-C, No. 12, 12/1/95, pp. 1709-1716.

The filing of this Statement is not to be construed as a representation that a search has been made regarding the claimed invention (37 C.F.R. §1.97(g)) or that no other possible material information exists. In addition, the filing of this Information Disclosure Statement is not to be construed to be an admission that the information cited in the Statement is, or is considered to be, material to patentability (37 C.F.R. §1.97(h)).

The Commissioner is hereby authorized to charge payment for any additional fees associated with this communication or credit any over payment to Deposit Account No. 16-1350.

Respectfully submitted,

  
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Joseph V. Gamberdell, Jr.

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October 29, 2003

Date

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<b>INFORMATION DISCLOSURE CITATION FORM FOR PATENT APPLICATION (FORM PTO-1449) (Substitute)</b>		Docket No.: 871-011564-US(PAR)	Serial No.:			
		Applicant(s): VANDENHOECK et al.				
		Filing Date: Herewith	Group:			
<b>U.S. PATENTS</b>						
Initials	Patent Number	Issue Date	Name	Class	Sub-class	Filing date
<b>U.S. PATENT PUBLICATIONS</b>						
Initials	Publication No.	Pub. Date	Name	Class	Sub-class	Filing Date
	2001/0004761 A1	6/21/01	Zehavi	714	784	2/14/01
<b>FOREIGN PATENT DOCUMENTS</b>						
Initials	Document Number	Date	Country	Name		Translation? Yes/No/n/a
	WO 01/93431 A1	12/6/01	PCT	Qualcomm Incorporated		n/a
<b>OTHER DOCUMENTS (Title, Author, Date, Pages, Etc., if known)</b>						
	"A 16-Bit Digital Signal Processor With Specially Arranged Multiply-Accumulator For Low Power Consumption", Katsuhiko et al., IEICE Transactions on Electronics, Institute Of Electronics Information And Comm. Eng., Vol. E78-C, No. 12, 12/1/95, pp. 1709-1716.					
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